



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/054,653 Confirmation No.: 9448  
First Named Inventor: Bulucea, Constantin Filing Date: 18 January 2002  
Group Art Unit: 2814 Examiner: Farahani, D.  
Atty. Docket No.: NS-5127 US  
Title: Gate-Enhanced Junction Varactor With Gradual  
Capacitance Variation  
Assignee(s): National Semiconductor Corporation

Mountain View, California  
18 January 2006

**MAIL STOP ISSUE FEE  
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**SUBMISSION OF FORMAL DRAWINGS**

Sir:

Enclosed for the above patent application are twenty-five sheets of formal drawings consisting of Figs. 1a - 38. The enclosed drawings incorporate the revisions requested in (a) the Preliminary Amendment submitted 28 October 2003 and (b) the accompanying Amendment to Drawings under 37 CFR 1.312. If these revisions are acceptable, please substitute the enclosed drawings for the informal drawings now on file.

Please telephone Attorney for Applicant(s) at 650-964-9767 if there are any questions regarding these drawings.

**EXPRESS MAIL LABEL NO.:**

**EV 743 584 881 US**

Respectfully submitted,

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